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TRANSMITTAL OF FORMAL DRAWINGS

Docket No.
BUR920020014

In Re Application Of: Steven Voldman

Serial No.

09/683,985

Filing Date

March 8, 2002

Confirmation No.

Examiner

Ori Nadav

Art Unit

2811

Invention: A METHOD AND STRUCTURE FOR LOW CAPACITANCE ESD ROBUST DIODES

Address to:
Assistant Commissioner for Patents
Washington, D.C. 20231

Transmitted herewith are:

4 sheets of formal drawing(s) for this application.

☒ Each sheet of drawing indicates the identifying indicia suggested in 37 CFR Section 1.84(c).


Signature

Anthony J. Canale, Reg. #51,526
IBM Corporation
1000 River Street, 972E
Essex Junction, VT 05452

Dated: June 7, 2004

I certify that this document and attached formal drawings
are being deposited on June 7, 2004 with the
U.S. Postal Service as first class mail under 37 C.F.R. 1.8
and addressed to the Assistant Commissioner for Patents,
Washington, D.C. 20231.


Signature of Person Mailing Correspondence

Arnette Dodge

Typed or Printed Name of Person Mailing Correspondence